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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): J. G. Walacavage et al.

Serial No: 09/966,121

Group Art Unit: 2123

Filed: September 28, 2001

Examiner: Not Assigned

Title: METHOD OF EMULATING MACHINE TOOL BEHAVIOR FOR PROGRAMMABLE LOGIC  
CONTROLLER LOGICAL VERIFICATION SYSTEM

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August 18, 2004

Daniel H. Bliss

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**STATUS LETTER**

Sir:

The above-identified patent application has been on file with the United States Patent and Trademark Office for more than one year and no office actions have been received. Attorney for Applicants request the status of this application including the name and telephone number of the Examiner.

Respectfully submitted  
By: [Signature]  
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